

Analog Mixed Signal IC Design Director

Interested parties please submit resumes to sabrina.chen@conexant.com

Position: Analog Mixed Signal IC Design Director

Location: Beijing/Shanghai, China

Primary Objectives:

The position is responsible for supporting or managing and directing a team of analog designers in China core engineering business unit of Conexant Systems Inc. Major responsibilities include supporting the design of Analog Front-End and Analog IP for SOC, managing and recruiting an engineering team covering Analog IC design and layout.

Specific Responsibilities:

The management of the engineering team, responsible for the development and support of current and future products development, includes:

- Mentoring, training and improving analog design skills, characterization and testing for the current team.
- Recruiting, managing, and training the team from its current size of around 10+ employees.
- Work with US AMS team and System team to define and architect new products including specification definition, design trade off and resources planning.
- He / she will be responsible for the successful design and implementation of a wide range of high performance analog integrated circuits for personal telecommunications.
- Planning the team's projects and managing projects to be on time and on budget.
- Planning the capital and labor budgets required to support the engineering activities including new programs.
- Managing the analog Mixed Signal IC design process, including: project scheduling, device specification, detailed design, verification, DFM/DFT analysis, ESD design, prototyping, characterization, reliability testing, and production test development.
- Working with QA to qualify the entire hardware solutions prior to general availability; debug problems and modify designs as appropriate.
- Working with Operations to transfer the design into our internal manufacturer for volume production.
- Implementing good design practices and other methodologies.

Position Requirements:

Required Attributes: This is a highly technical position, and the successful candidate must be able to quickly command respect from a team of senior engineers for his/her engineering knowledge, judgment, and experience. The successful candidate will be measured on his/her ability to plan a complex workload in a rapidly changing environment, deliver high quality products on time and within budget, and maximize the productivity of the design team. Specific required and desired attributes include:

- BSEE degree required, MSEE or PhD in EE preferred.
- 10 years of hands on experience in IC design analysis, modeling, and qualification of analog/mixed-signal products, and management of Project team.
- Leader with outstanding technical and analytical skills and commitment to success.
- Proven ability to manage product development activity (from inception to production release) per an aggressive schedule; able to make trade-offs that optimize around a project's primary goals without being "sloppy."
- Experience with best practices and other methodologies.
- Silicon process/Fab experience is a plus.
- Strong understanding of semiconductor device physics.
- Experience working closely with product management, QA/DVT and operations.
- Extremely driven individual that is able to successfully set priorities, drive decisions and get closure on recommendations and issues.
- Excellent interpersonal, organizational, and leadership skills.
- Excellent scheduling, budgeting, resource loading, and overall planning skills.
- Strong verbal and written communication skills.
- Self-motivated and able to work well autonomously.