



Spreadtrum Communications, a promising pre-IPO company, was founded in 2001 by a group of successful entrepreneurs. It is now a leading fabless semiconductor company developing and marketing innovative digital wireless communications products. The company provides 2G/2.5G and 3G baseband chipsets, protocol stack software and application software, total solution for wireless equipment manufacturers, mobile terminal vendors and semiconductor companies. Spreadtrum Communications is currently experiencing high-speed growth in both its revenue and size, and this rapid growth is becoming a legend among Silicon Valley start-ups. Spreadtrum Communications is expanding its Silicon Valley core R&D center and we sincerely invite you, the most talented minds, to share our dream and success.

We offer competitive compensation package (stock option, medical, dental & life insurance, FSA with employer contribution, 401k, holidays, vacations, etc.).

Job Title: Staff Physical Design Engineer
Job Location: Sunnyvale, California
Position Type: Full-time Employee

Responsibilities:

Design and implement low-power communication SOC chip from RTL-GDSII. Good knowledge on the low-power design techniques and hand-on experience on low-power design tape-out. Implement the low-power design in 90nm/65nm using multi-VDD, Power Islands, and MTCMOS. Hierarchical design experience on Partition and Power/Xtalk/Timing/DFT/DFM closure is required.

Requirements:

- At least 5 years hands-on experience in physical design.
- Familiar with Linux/Unix environment and script languages.
- Good communication skills and team player.
- Familiar with Synopsys or Cadence physical design flow.
- BS or M.S. Degree in Physical or Electrical Engineering

We are an Equal Opportunity Employer.

Please submit your resume to hr@spreadtrum.com with job title.

Please check out our website for additional jobs and information:

www.spreadtrum.com

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