



Spreadtrum Communications, a promising pre-IPO company, was founded in 2001 by a group of successful entrepreneurs. It is now a leading fabless semiconductor company developing and marketing innovative digital wireless communications products. The company provides 2G/2.5G and 3G baseband chipsets, protocol stack software and application software, total solution for wireless equipment manufacturers, mobile terminal vendors and semiconductor companies. Spreadtrum Communications is currently experiencing high-speed growth in both its revenue and size, and this rapid growth is becoming a legend among Silicon Valley start-ups. Spreadtrum Communications is expanding its Silicon Valley core R&D center and we sincerely invite you, the most talented minds, to share our dream and success.

We offer competitive compensation package (stock option, medical, dental & life insurance, FSA with employer contribution, 401k, holidays, vacations, etc.).

Job Title: ASIC Design Engineer
Job Location: Sunnyvale, California
Position Type: Full-time Employee

Responsibility:

The candidate will be responsible for design and implementation of low-power logic in wireless communications ASIC chips.

Requirements:

- 2 to 3 years experience in ASIC design.
- Bachelor degree in E.E. or equivalent.
- Good understanding of DVB-H/DAB, WiMax, or 802.11a/g/n, etc. WCDMA/HSDPA experience is also good to have.
- Experience with logic design, simulation, synthesis and ASIC design flow.
- Proficiency in Verilog and its simulation environment.
- Experience with wireless and low-power design.
- Self-motivated and good team player.
- International travel is expected.

We are an Equal Opportunity Employer.

Please submit your resume to hr@spreadtrum.com with job title.

Please check out our website for additional jobs and information:

www.spreadtrum.com

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