

EADTRUM 众志成城 超越梦想

Spreadtrum Communications, a promising pre-IPO company, was founded in 2001 by a group of successful entrepreneurs. It is now a leading fabless semiconductor company developing and marketing innovative digital wireless communications products. The company provides 2G/2.5G and 3G baseband chipsets, protocol stack software and application software, total solution for wireless equipment manufacturers, mobile terminal vendors and semiconductor companies. Spreadtrum Communications is currently experiencing high-speed growth in both its revenue and size, and this rapid growth is becoming a legend among Silicon Valley start-ups. Spreadtrum Communications is expanding its Silicon Valley core R&D center and we sincerely invite you, the most talented minds, to share our dream and success.

We offer competitive compensation package (stock option, medical, dental & life insurance, FSA with employer contribution, 401k, holidays, vacations, etc.).

Job Title: ASIC DFT Engineer Job Location: Sunnyvale, California Position Type: Full-time Employee

Responsibility:

This is a key design engineer position in ASIC design team focusing on ASIC design for testability. The candidate will be responsible for the DFT implementation of the chip. He will also be responsible for the production test and yield enhancement of the chip.

Requirements:

- 5 years experience in ASIC design for testability and yield enhancement.
- Bachelor degree in E.E. or equivalent.
- Solid knowledge in ASIC design flow.
- Experience with the DFT design of multiple chips and yield rate enhancement.
- Experience with the challenges of DFT and yield issues in sub micron and mix signal chip
- Excellent capability in using Synopsys design compiler and design for test tools.
- Self-motivated and good team player.
- International travel is expected.

We are an Equal Opportunity Employer.

Please submit your resume to **hr@spreadtrum.com** with job title.

Please check out our website for additional jobs and information: www.spreadtrum.com