

Immediate positions are available, experienced people are needed to join this newly NASDAQ listed company to design semiconductor chips in the area of video and audio. Specifically, we have the following positions available:

### **1. VLSI design, Staff System Engineers /Project Manager**

Experience : 5~10 years working experience in wireless communication ASIC design  
Hands on experience in channel coding, DVB-T/C, ATSC, silicon tuner and RF system design, strong background in telecom and wireless system. Viterbi, RS decoder, channel estimation, channel correction, AGC control logic and NCO design is preferred. Familiar with Verilog, VHDL and VCS/NC simulator. Debugging skill and teamwork are necessary.

Familiar with Verilog, Matlab and C.

### **2. Algorithm / System Architects / Project leader**

Experience : 5~10 years working experience in DVB or ATSC system and VLSI design.  
Hands on experience in DVB-T/S system simulation, channel coding, wireless baseband, silicon tuner and RF system design, matlab and C modeling of full chip and familiar with testing and measurement requirement. Tools :Matlab, C and VHDL/Verilog (Ph.D. preferred )

### **3. Principal Mixed-Signal design Engineer**

5~10 years hands on experience in high speed ADC design, VCO, Bandgap, standard PLL and fractional PLL design in digital communication system. Experience also include PreAmp, AGC, LNA and low power design technique.

### **4. CMOS RF design engineers /Project Manger / Sr. Principal Engineers**

10 or more years of experience in RF IC Design for wireless communication systems.  
Need to have CMOS RF experience. RF blocks used in Transmitter and receivers, Front-end LNA and mixers, basedband filter and PLL, NCO design. ATSC/DVB/NTSC Silicon Tuner design experience is highly desirable.

### **5. Project leaders/Video architects**

Experience : 5~10 years + working experience in video decoder design, specifically in NTSC/PAL/SECAM decoder, 2D comb and 3D comb filtering algorithm. Understanding of various video standard. AGC, Color demodulation, weak signal and video post processing and VBI slicing design experience is required.

Must Familiar with Matlab, C and verilog

## **6. Principal design Engineers / Audio Architects**

Experience : 7 years + working experience in audio decoder design, specifically in NTSC FM stereo including BTSC, A2, NICAM decoding algorithm and design experience.  
Must Familiar with Matlab, C and verilog

We also need analog engineers. Duties: design analog and mixed-signal integrated circuits in these exciting product areas, mixed-signal audio and video products, precision audio A/D and D/A circuits, high-speed and high-precision/micropower amplifiers and comparators, linear and Class D audio power amplifiers, and low-drift/high-precision voltage references.

[Ph.D. students near completion from an accredited university are also welcome.](#)

We are a public company but with Silicon Valley Start-up excitement. We offer competitive compensation package to all qualified applicants.